

INFORMATION DISCLOSURE CITATION	Atty. Docket No. 011644	Serial No. 10/006,238
	Applicant(s): MATSUZAKI, Yasurou et al.	
	Filing Date: December 10, 2001	Group Art Unit: 2631

**U.S. PATENT DOCUMENTS**

Examination Initial	Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
____	AA	<div style="text-align: right;"> RECEIVED DEC 30 2003 Technology Center 2600 </div>				
____	AB					
____	AC					
____	AD					

FOREIGN PATENT DOCUMENTS

Document No.	Date	Country	Translation (Yes or No)
____	AE		
____	AF		
____	AG		
____	AH		

OTHER DOCUMENTS

/F.H./ ____	AI	Benini et al.; "Synthesis of Low-Overhead Interfaces for Power-Efficient Communication over Wide Buses"; Design automation Conference, 1999 Proceedings, IEEE, June 1999, pages 128-133 .
/F.H./ ____	AJ	Khara et al.; "High-Level Synthesis with SDRAMs and RAMBUS DRAMS"; <u>IEICE TRANS. FUNDAMENTALS</u> ; Vol. E82-A, No. 11; November 1999; pages 2347-2355.
/F.H./ ____	AK	Copy of European Patent Office Communication including Partial European Search Report for corresponding European Patent Application No. 01310305 dated December 16, 2003.
Examiner	/Fernando Hidalgo/	Date Considered 04/14/2010

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /F.H./